

RS LOGIX 500 Starter - ELEC 141
Printing Ladder Diagrams - Print Reports

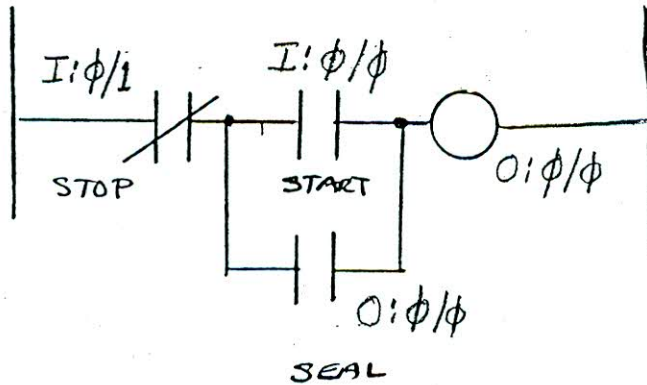
1. Click on "FILE"
2. Click on "REPORT OPTIONS"
3. "CONFIGURATION" should be highlighted, if not click on "CONFIGURATION"
4. Click on "DESELECT ALL." Click ☒ "Program Files". Click Apply
5. Click "LADDER SETUP" Click:
 - (a) Binary Bit Display Mode, ☐ Bit
 - (b) Bit Address Format, ☐ Single Line
 - (c) Address Description, Alignment:
 <Center> (Optional)
 - (d) Misc: ☒ Show 3D instructions (Optional)
 - (e) Click Apply
6. Click On: "Program Files":
 - (a) Click On: "Select Files"
 - (b) Click On: "Deselect All"
 - (c) On the Program File/Run Chart, Check off the Ladder Diagram Numbers you want printed. For example LAD 2, LAD 6 (if used), etc.
 - (d) Click "APPLY", then click "Print Preview"
7. Scale up or Scale down each page to your size preference; then print.
8. Save

NOTE: You may want to try other report options.

EXERCISE TWO

CLEAR LADDER DIAGRAM

BASIC CONTROL CIRCUIT

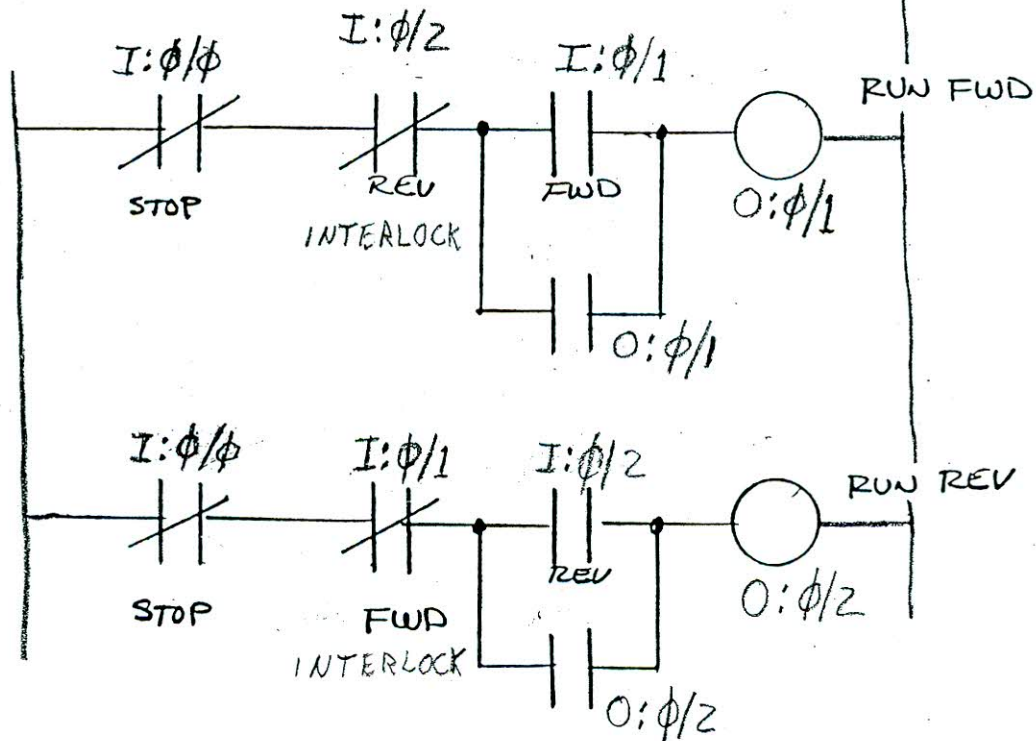


- SAVE AS: STDCTRL1.RSS
- VERIFY PROJECT
- DOWNLOAD
- TEST OPERATION

FORWARD / REVERSE CONTROL

CLEAR LADDER DIAGRAM

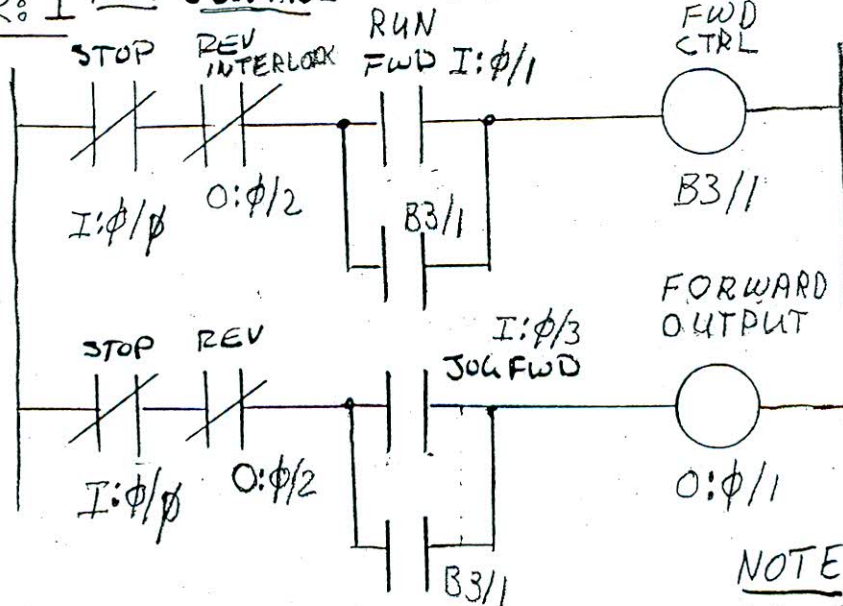
- SAVE AS: FWDREVPBEI1.RSS
- VERIFY PROJ.
- DOWNLOAD
- TEST



NOTE: BLOCK 1 & 2 ARE ONE PROGRAM

EXERCISE-THREE

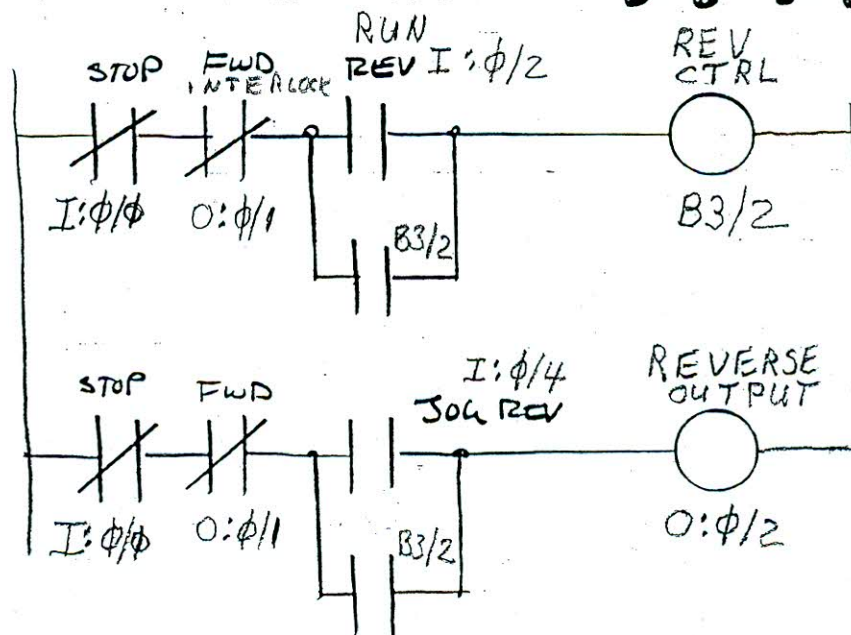
BLOCK 1: FWD CONTROL



NOTE: B3/1, B3/2
ARE "INTERNAL"
COILS

BLOCK 1 ⇒ FWD CONTROL

BLOCK 2: REV CONTROL



SAVE AS:
FRCOILEI1.RSS
• VERIFY
• DOWNLOAD
• TEST

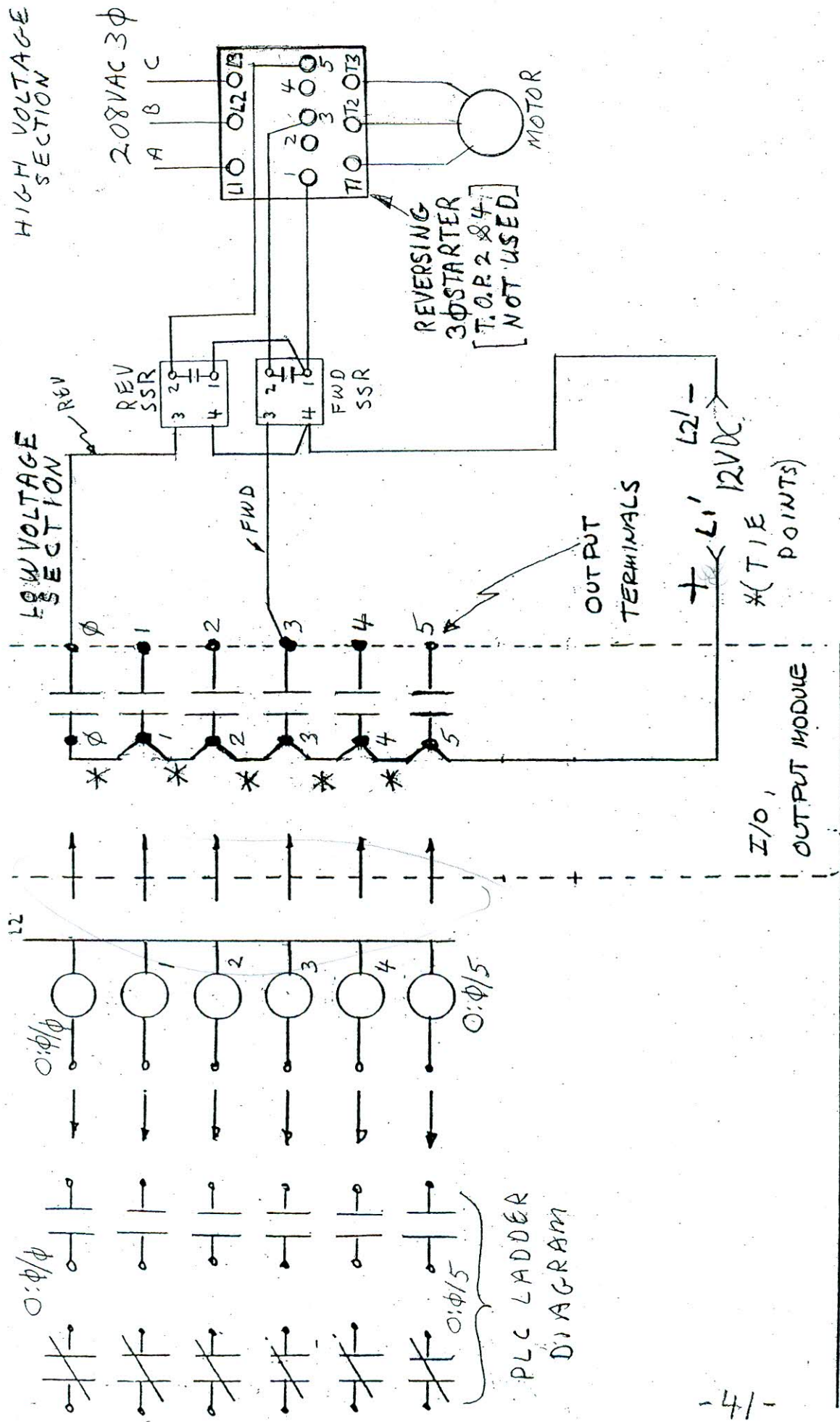
FWD / REV

JOL FWD

JOL REV

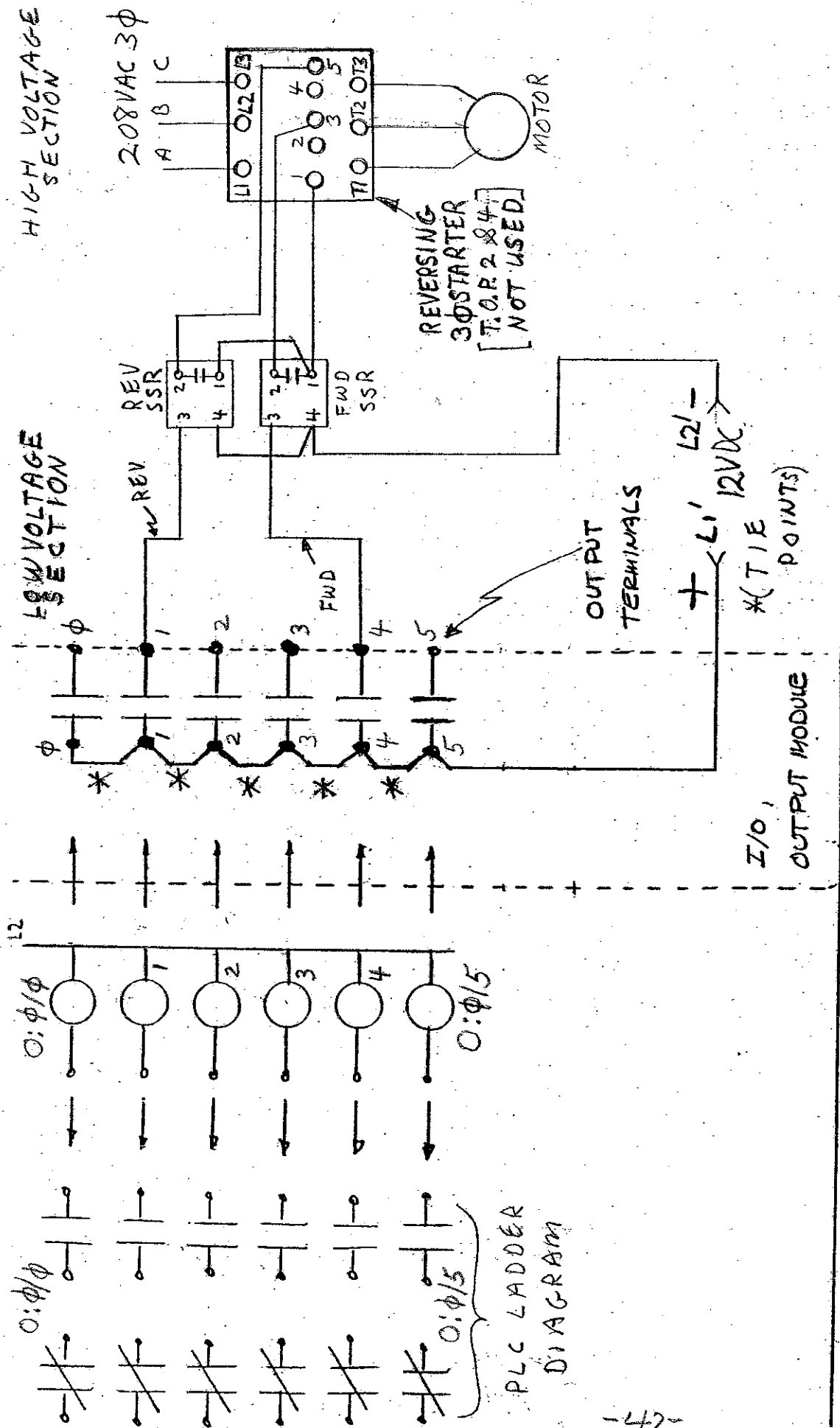
LOW VOLTAGE OUTPUT TO SSR, CONTROLLING, REVERSING STARTER

GROUP 1

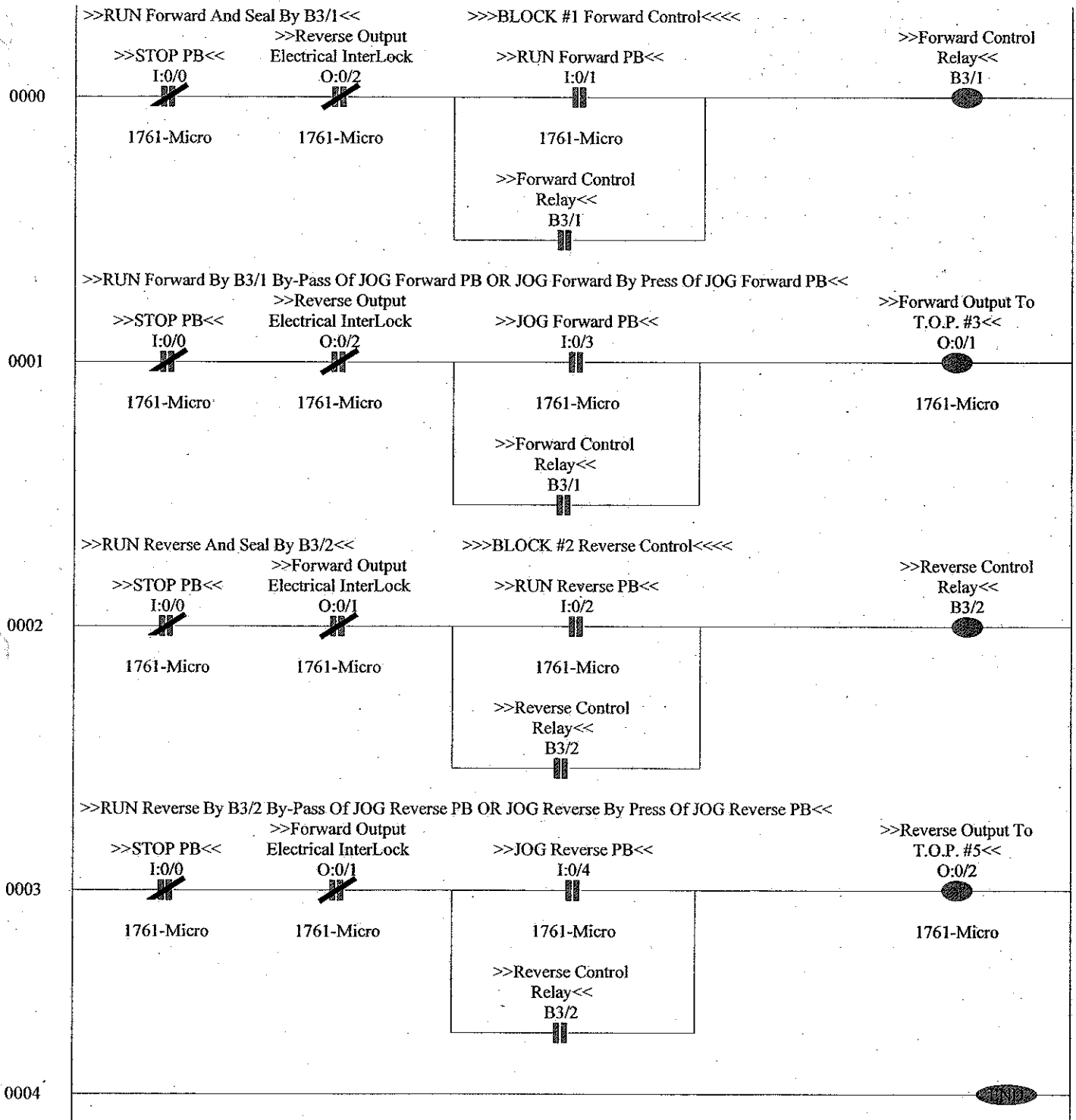


PLC LOW VOLTAGE OUTPUT TO SSR, CONTROLLING REVERSING STARTER

GROUP 2

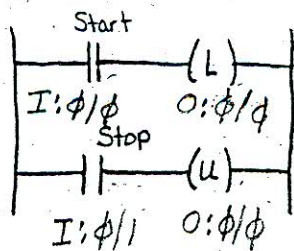


D 2 - FRDJOGCEIL - Forward/Reverse/Dual Jog/Coil Electrical InterLock --- Total Rungs in File =



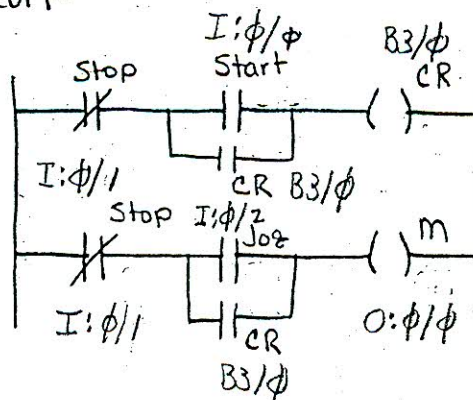
43

Standard Control Circuit w/ Latch and Unlatch: ADD RUNG COMMENTS IN EACH RUNG. DATE PROGRAM TITLE.



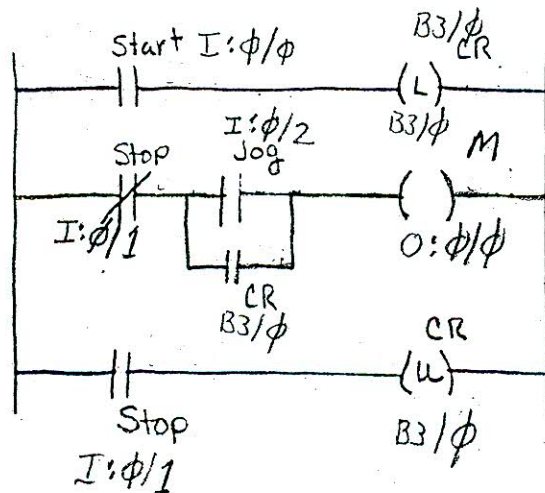
- SAVE AS: STDCTRL2.RSS
- VERIFY, DOWNLOAD, TEST

Jog Circuit



SAVE AS: JOGCR1.RSS

Jog Circuit with Latch/Unlatch Coils



- SAVE AS: JOGLU1.RSS
- VERIFY, DOWNLOAD, TEST

*FOR EACH PLC CIRCUIT YOU IMPLEMENT OR DESIGN SET-UP A INPUT/OUTPUT/ INTERNAL ADDRESS ASSIGNMENT CROSS REFERENCE CHART. SEE PAGE 55 FOR AN EXAMPLE.

SLC-100 LADDER DIAGRAMS

Industrial Controls

4/5/95

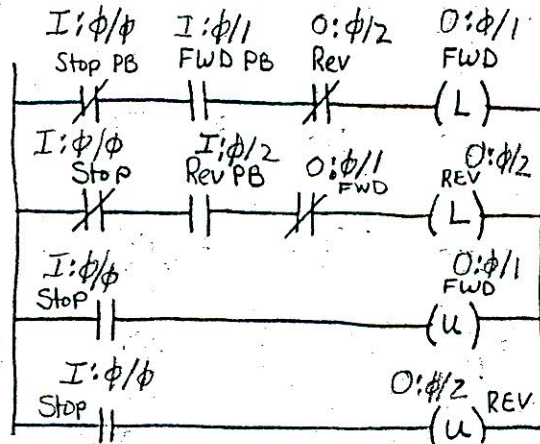
* DATE EACH PROGRAM TITLE *

1. FOR/REV/STOP with latch/Unlatch coils coil interlock

Filename FRLUCEI (Forward Reverse Latch Unlatch Coil Interlock)

Anti Plug must stop before change direction.

* ADD RUNG COMMENTS *



• SAVE AS FRLUCEI.RSS

• VERIFY

• DOWNLOAD

• TEST

2. FOR/REV/STOP With latch/Unlatch Coils and Push Button Interlock

Filename FRLUPBEI (Forward Reverse Latch/Unlatch PB interlock)

* ADD RUNG COMMENTS *

