Electrical Engineering Tech. (A.A.S.)
Canino School of Engineering Technology
Spring 2017 – Fall 2017 Assessment Report



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What Was Assessed? Program Student Learning Outcomes (PSLO) List:

- PSLO # 1 Written/Oral/Visual Presentation
- PSLO # 2 Computer Aided Design
- PSLO # 3 Principles & Appl. of AC & DC Power Circuit Analysis
- PSLO # 4 Principles & Appl. of Digital & Analog Electronics
- PSLO # 5 PLC Principles & Applications
- PSLO # 6 Effective Teamwork Skills
- PSLO # 7 Ability to use Test Equipment
- PSLO # 8 Printed Circuit Board Design, Fab. and Testing
- PSLO # 9 Professional and Ethical Responsibilities

Where Were the Program Student Learning Outcomes Assessed?

- PSLO # 1 Written/Oral/visual Presentation
 - ELEC 109 (17)*, ELEC 203 (12), ELEC 213 (16) * = Sample (Class) Size
- PSLO # 2 Computer Aided Design
 - ELEC 213 (16), SOET 116 (16)
- PSLO # 3 Principles & Appl. of AC and DC Power Circuit Analysis
 - ELEC 109 (17), ELEC 215 (16)
- PSLO # 4 Principles & Appl. of Digital and Analog Electronics
 - ELEC 165 (17), ELEC 213 (16), ELEC 231 (16)
- PSLO # 5 PLC Principles & Applications
 - ELEC 141 (17)

Where Were the Program Student Learning Outcomes Assessed? (Continued)

- PSLO # 6 Effective Teamwork Skills
 - ELEC 203 (12)
- PSLO # 7 Ability to use Test Equipment
 - ELEC 166 (17), ELEC 213 (16), ELEC 243(12)
- PSLO # 8 Printed Circuit Board Design, Fabrication and Testing
 - ELEC 161 (17)
- PSLO # 9 Professional and Ethical Responsibilities
 - SOET 377 (12)

How Was the Assessment Accomplished?

Student work assessed:

- Homework questions
- Solutions of exam questions
- Lab Reports
- Oral presentations/ Research papers

Measurement strategy:

- Rubrics used for oral presentations, research papers, and lab reports
- Percent of students who scored above a target value on exam and homework questions

Sample size:

• All students who took the designated assessed courses; class (sample) size shown within the parenthesis following the course number on the two previous slides.

Actual Assessment Measurement Data for Each Program Student Learning Outcome for 2017

Program Student Learning Outcome	Numb _#	oer of Measures (#) (Met/ Not Met)	Percent Meeting Target
PSLO #1 Written Presentation	3	(3/0)	100
PSLO #2 Computer Aided Design	1	(1/0)	100
PSLO #3 Prin AC & DC Power	4	(4/0)	100
PSLO #4 Prin Dig. & Analog Elect.	4	(4/0)	100
PSLO #5 PLC Principles and App.	3	(3/0)	100
PSLO #6 Effective Team Work Skills	3	(3/0)	100
PSLO #7 Able to use Test Equipment	7	(7/0)	100
PSLO #8 Printed Circuit Board Design	4	(4/0)	100
PSLO #9 Professional & Ethical Respo	n. 4	(4/0)	100

Data-driven decisions: How the program has or plans to "close the loop" based on these results.

- Continue to revise the course curriculum to better align with the program's objectives according to the SLO outcomes
- Continue to revise the related courses to better align with the student learning outcomes
- Advise students in the program to seek additional instructional support when needed, e.g. tutoring.

What resources were used or have been requested to close the loop?

- Resources Used:
 - •Individual faculty time revising within a course.
- Resources Requested:
 - None at this time.

What changes would you make to the Assessment Process?

- Add an additional measure to the Program
 Assessment component of Taskstream to evaluate our Final Product (The Graduates).
 - Need to keep in touch with them (First five Years).
 - ■Collect feedback from them.